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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/649,260	08/25/2003	Anand G. Dabak	TI-35237	7206
23494	7590	04/21/2010	EXAMINER	
TEXAS INSTRUMENTS INCORPORATED			PHAN, MAN U	
P O BOX 655474, M/S 3999				
DALLAS, TX 75265			ART UNIT	PAPER NUMBER
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Please find below and/or attached an Office communication concerning this application or proceeding.

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Advisory Action

1. The affidavit, exhibit or request for reconsideration has been considered but does not place the application in condition for allowance because:

Applicant's arguments are not persuasive. It's the examiner's position that the reference is applied herein for the teaching of a novel method and system for receiving transmission in a wireless communication system. As presented in the last office action, the Applicant's attention is directed to Fig. 1 of Nakamura et al. (US#6,920,173) for the structure of an interference cancellation unit 200 includes a despread 201 for multiplying a receive signal S by a despreading code that is identical with the spreading code, thereby outputting a despread signal; a demodulator 202 for demodulating "1", "0" of user data and control data on the basis of the result of despreading; an attenuator 203 for attenuating the demodulated signal by multiplying the result of demodulation by a damping coefficient that conforms to the degree of reliability; a re-spreader 204 for spreading the demodulated signal again to thereby output an interference replica; and a symbol-replica interface 205 for creating and sending a symbol replica (Col. 12, lines 40 plus). In the same field of endeavor, Kim (US#6,810,007) discloses in Fig. 1B a block diagram of an orthogonal frequency division multiplexing (OFDM) receiving system for receiving a signal transferred from the transmission system of Fig. 1A. The receiving system of Fig. 1B includes an LPF 110, an analog-to-digital converter (ADC) 111, a cyclic prefix remover 112, an FFT 113, a Q-ary demodulator 114, a parallel-to-serial converter (PSC) 115, and a block decoder 116. The LPF 110 in the receiving system filters the transmitted OFDM signal at the same frequency band as the LPF 106 of the transmission system. The ADC 111 converts a filtered signal into a digital signal, and the cyclic prefix

remover 112 removes the cyclic prefix added in the transmission system. The FFT 113, the Q-ary demodulator 114, the PSC 115 and the block decoder 116 perform inverse processes of the processes performed by the counterparts of the transmission system, thereby restoring an OFDM signal (See Fig. 2A; Col. 3, lines 39 plus).

Applicant argument with respect to the rejected claims 1, 16 that *“as stated in Applicant’s specification, the despreader reduces the spectral space consumed by the received signal, so that the OFDM demodulator can operate on a smaller amount of information, thus reducing the number of computations required to perform the OFDM demodulation”*. However, It is the claims that define the claimed invention, and it is claims, not specifications that are anticipated or unpatentable. *Constant v. Advanced Micro-Devices Inc.*, 7 USPQ2d 1064. Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

Examiner maintains that the references cited and applied in the last office actions for the rejection of the claims 1-26 are maintained in this office action. The final rejection mailed on November 26, 2008 is therefore maintained.

Mphan.

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Primary Examiner, Art Unit 2475